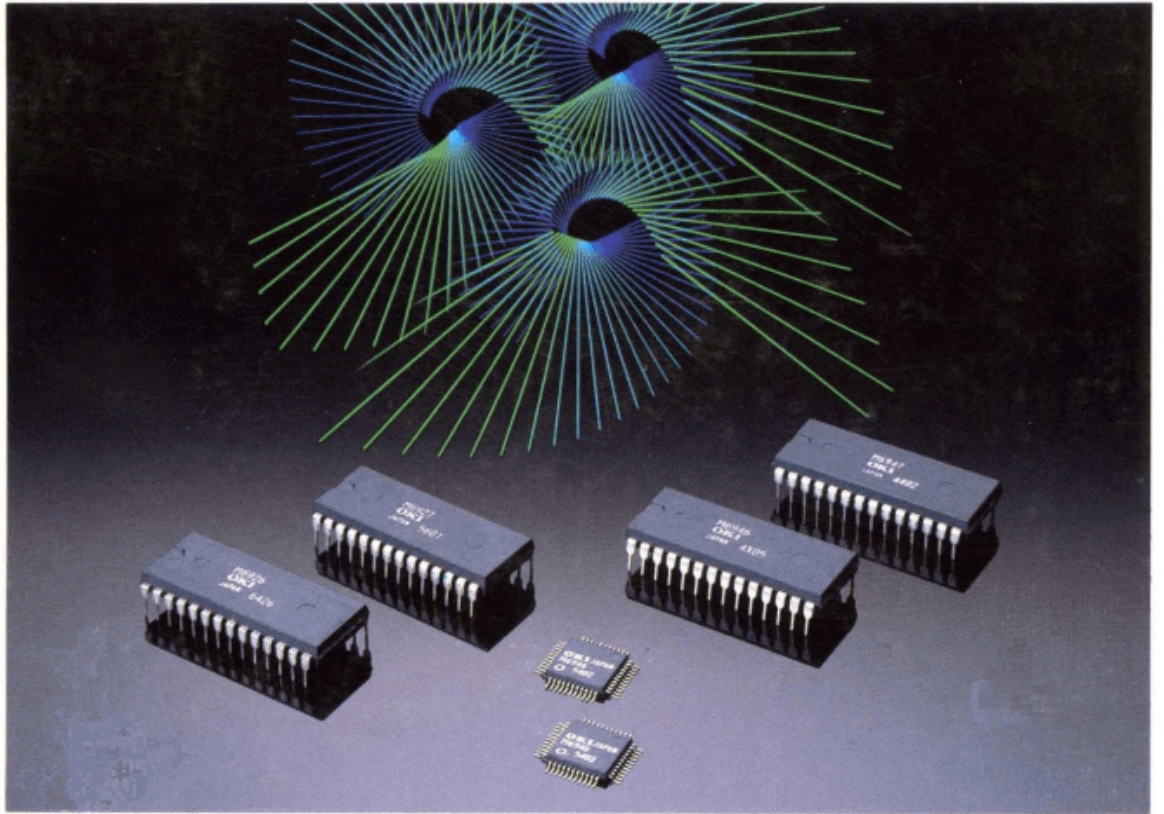


OKI

TELECOM LSI DATA BOOK

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MSM77C20

DIGITAL SIGNAL PROCESSOR

GENERAL DESCRIPTION

The MSM77C20 is an LSI designed for the purpose of digital signal processing in the field of speech processing and telecommunications.

Since this LSI is the one-chip microcomputer contained the ROM, RAM, ALU, and multiplier, it is applicable to different systems by re-writing contents of the program MASK ROM.

FEATURES

- Instruction cycle : 250 ns
- Instruction ROM : 512 words \times 23 bits
- Data ROM : 512 words \times 13 bits
- Data RAM : 128 words \times 16 bits
- Multiplier : 16 bits \times 16 bits
= 31 bits
- Input/Output
Parallel Port
Serial Ports
DMA transfer
- CPU interface 8080 series
- 5 V single power supply
- 3-micron CMOS
- 28-pin ceramic DIP
- 28-pin plastic DIP

FUNCTIONAL BLOCK DIAGRAM

